## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| ncash.  | 10/051-624   |
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| Application Serial No   | J9/00 I3034  |
| Filing Date   | lay 5, 2001  |
| InventorAlan R  | Reinberg     |
| Misron Tools  | ology Inc    |
| Assignee Micron Techr   | lology, inc. |
| Group Art Unit  | Unknown      |
| Examiner  | Unknown      |
| LAMINIE   | MI22-1704    |
| Attorney's Docket No  | 101122-17 07 |
| Title: Microelectronic Device Fabricating Method, Integrated Circuit, and | ·i           |
|   |              |

Intermediate Construction

## INFORMATION DISCLOSURE STATEMENT

References -- See Attached Form PTO-1449

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449. No admission is made regarding whether all the submitted references are prior art.

The listed references were cited by, or submitted to, the Office in the parent, co-pending application of the above-identified application. The above-identified application is a divisional application of co-pending application Serial No. 09/561,794, filed May 1, 2000. Such prior disclosure is sufficient for the above-identified application as far as copies of the references are concerned. 37 C.F.R. § 1.98(d) and MPEP § 609(2).

Citation of these references is respectfully requested.

Respectfully submitted,

Dated: 12 Sep 2001

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